

1 SEM TDC CSC G 1 (N/O)**2016**

(November)

COMPUTER SCIENCE

(General)

Course : 101

(New Course)

(Computer Organization and Architecture)Full Marks : 48Pass Marks : 14

Time : 2 hours

*The figures in the margin indicate full marks
for the questions***Answer Question Nos. 1 and 2, and any six
from the rest**

- | | | |
|--------|---------------------------|---|
| 1. (a) | What is logic gate? | 1 |
| (b) | What is auxiliary memory? | 1 |
| (c) | What is number system? | 1 |
| (d) | What is register? | 1 |

(Turn Over)

2. (a) What is normalized floating point?
How can a real number be expressed
as binary in normalized floating point
mode? 2+3=5
- (b) What is cache memory? 1
- (c) What is universal gate? 1
- (d) What is instruction cycle? 1
3. Explain different types of logic gates using
diagram and truth table. 6
4. Write briefly about different addressing
modes. 6
5. Explain the concept of DMA transfer. 6
6. Write a short note on memory hierarchy. 6
7. Write briefly about 8085 microprocessor. 6
8. Write short notes on any *two* of the
following: 3×2=6
- (a) Instruction cycle
- (b) Stack organization
- (c) I/O interface
9. Write briefly about memory hierarchy. 6