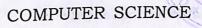
( Turn Over )

## 1 SEM TDC CSC G 1 (N/O)

2016

( November )



Elesche memory?

(General)

Course: 101

( New Course )

## (Computer Organization and Architecture)

Full Marks: 48
Pass Marks: 14

Time: 2 hours

The figures in the margin indicate full marks for the questions

Answer Question Nos. 1 and 2, and any six from the rest

		What is logic gate?		1
1.	(a)	Wilar is a summary		1
	(b)	What is auxiliary memory?		
	(0)	What is number system?		ol
	(c)			1
	(d)	What is register?		

2.	(a) What is normalized floating point?  How can a real number be expressed as binary in normalized floating point mode?  2+			
	(b) What is cache memory?	1		
	(c) What is universal gate?	1		
	(d) What is instruction cycle?	1		
3.	Explain different types of logic gates using diagram and truth table.			
4.	Write briefly about different addressing modes.			
5.	Explain the concept of DMA transfer.	6		
	Write a short note on memory hierarchy.			
	Write briefly about 8085 microprocessor.			
	Write short notes on any two of the following:			
	(a) Instruction cycle	-0		
	(b) Stack organization			
	(c) I/O interface			
9.	Write briefly about memory hierarchy.	6		

(Continued)

P7/16